

PATENT ABSTRACTS OF JAPAN



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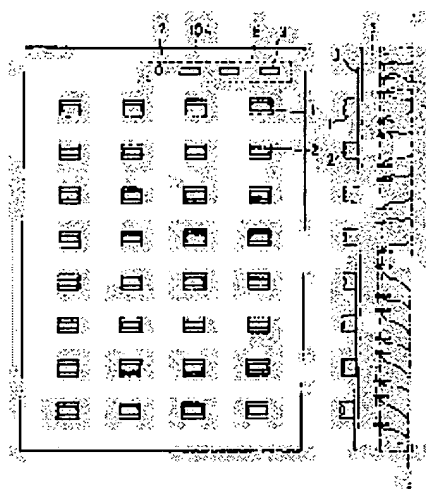
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(54) CARRIER BOARD FOR IC HAVING MEMORY

(57)Abstract:

PURPOSE: To enable the implementation of a function test of an IC with the separation of a supply portion and a sorting/housing section for the IC to be measured by carrying a memory on a carrier board of the IC to store necessary items, results of measurement and the like pertaining to an IC test.

CONSTITUTION: An IC2 to be measured is carried on an IC socket 1 provided at a specified position of a carrier board 104. In a contact pattern provided on the rear of the board 104, a contact probe pin 8 is connected to the board 104 to perform a transmitting or a receiving of power source, signal and memory data and other operations. Then, a display section 6 provided at a specified position of the board 104 is provided with a memory element 3 of a memory and a display means 7 and necessary items, results of measurement and the like, for example, lot numbers and measuring data pertaining to an IC test are stored into the element 3 to display 7. This facilitates the identification of ICs to be measured to separate a supply portion and a sorting/housing section for the ICs to be measured thereby permitting a function test of ICs.



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